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Digital Design Homework # 1 (Chapter 1) INSTRUCTIONS: o Show your work (i.e., how you derived your answer or the reason behind your thinking) in addition to your answer. o Homework and solution must be printed and solved on the same paper. o Deadline : Wednesday, 19/9/2012 (not late homework will be accepted)

## Digital Design Homework # 1 (Chapter 1)

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(1) ... 2013 in Chapter 7, Digital Design. 0. Sequential Circuits. Sequential circuits are circuits that have some type of memory to remember the past input values, they use latches and flip-flops.

### Chapter 7 | Computer Science Courses

9.3 Experiment 2: Digital Logic Gates 446 9.4 Experiment 3: Simplification of Boolean Functions 448 9.5 Experiment 4: Combinational Circuits 450 9.6 Experiment 5: Code Converters 452 9.7 Experiment 6: Design with Multiplexers 453 9.8 Experiment 7: Adders and Subtractors 455 9.9 Experiment 8: Flip-Flops 457 9.10 Experiment 9: Sequential Circuits 460

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## **Digital Systems Design Assignment Help and Homework Help**

Each chapter is a combination of theory followed by review exercises to be completed as traditional homework assignments. Full solutions to all of the review exercises are available in the last appendix. Procedures for labs then follow that allow the student to implement the concepts in a hands on manner. The materials required for the labs

## **Introduction to Digital Logic with Laboratory Exercises**

Tools you'll need: Aldec Active-HDL simulator Download site used for simulating the Verilog code, the website to download a free student version of the Aldec Active-HDL simulator.. ISE WebPACK Download site used for synthesizing the Verilog designs.. The Three Basic Gates. NOT gate inverts the logic state of input, so if x is 0, then y is 1 or if x is 1 then y is 0.

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